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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Applicant : Yangsung Joo  
Application No. : 10/783,976 Confirmation No. : 4392  
Filed : February 20, 2004  
For : DRIVING A DRAM SENSE AMPLIFIER HAVING  
LOW THRESHOLD VOLTAGE PMOS TRANSISTORS  
Group Art Unit : 2824  
Examiner : Pho M. Luu

New York, New York 10020  
July 1, 2005

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Sir:

The above-identified patent application was allowed on June 15, 2005. The Notice of Allowability included a Statement of Reasons for Allowance.

As part of the Statement, the Examiner stated (on page 2 of the Notice of Allowability) that "[t]here is no teaching or suggestion in the prior art" and then went on to set forth features from applicant's claims as quotes. However, the "quotes" are not exact, and to the extent that the language set forth by the Examiner in the form of a quote differs from the actual claim features, applicant respectfully submits that his invention should

be limited only by the claim language itself and not by the inexact quotes that appear in the Statement of Reasons for Allowance.

To assist the Examiner, applicant sets forth in the tables below his claim features as compared to the language "quoted" in the Statement of Reasons for Allowance:

| <b>Claim 58</b>   | <b>Statement of Reasons for Allowance</b>  |
|---|--|
| circuitry operative to raise said input terminal of said amplifier to said full supply voltage after said differential voltage develops between said pair of signal lines | the circuitry operative to raise the input terminal of the amplifier to the full supply voltage after the differential voltage develops between the pair of signal line in an electrical circuit |

| <b>Claim 69</b>   | <b>Statement of Reasons for Allowance</b>  |
|---|--|
| raising said input terminal of said amplifier to a full supply voltage after said differential voltage develops between said pair of signal lines | the circuitry operative to raise the input terminal of the amplifier to the full supply voltage after the differential voltage develops between the pair of signal line in an electrical circuit |

| <b>Claim 80</b>   | <b>Statement of Reasons for Allowance</b>  |
|---|--|
| means for raising said input terminal of said amplifier to a full supply voltage after said differential voltage develops between said pair of signal lines | the circuitry operative to raise the input terminal of the amplifier to the full supply voltage after the differential voltage develops between the pair of signal line in an electrical circuit |

Applicant respectfully invites the Examiner to issue a revised Statement of Reasons for Allowance using exact quotes from the claims.

Respectfully Submitted,

Evelyn C. Mak

Evelyn C. Mak  
Registration No. 50,492  
Attorney for Applicant

Fish & Neave IP Group  
Ropes & Gray LLP  
Customer No. 1473  
1251 Avenue of the Americas  
New York, New York 10020-1105  
(212) 596-9000

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